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ABSTRACT OF THE DISCLOSURE

A multiplexed pixel display includes a plurality of pixel electrodes, a plurality of storage elements, a first voltage supply terminal, a second voltage supply terminal, a common electrode, and a plurality of multiplexers each selectively coupling an associated one of the pixel electrodes with one of the first voltage supply terminal and the second voltage supply terminal responsive to a value of a data bit stored in an associated one of said storage elements. A controller is configured to sequentially write each bit of multi-bit data words to the storage elements, and assert, while each bit is stored in the storage elements, a first predetermined voltage on the first voltage supply terminal, a second predetermined voltage on the second voltage supply terminal, and a third predetermined voltage on the common electrode, for a time dependent on the significance of the stored bit. Various alternate controllers facilitate the use of additional driving schemes.